

Research and Testing of 5G wireless Scheduler Algorithms

Team: sddec21-15

Members: Anh To, Bradley Norman, Haan Zilmer, Elias Zougmore

Faculty Advisor: Hongwei Zhang

Client: Hongwei Zhang

Intro/Motivation

Advancements in 5G technology have led to an increase in demand for qualified engineers with the ability to develop and prototype advanced wireless solutions. 5G wireless networks are expected to enable not only Gbps mobile connectivity but also machine-type communications for smart agriculture, connected and automated vehicles, smart grid, Industry 4.0, and AR/VR. Our project is in a research capacity, so while we will not be solving any specific problem, we will be looking into ways to improve the scheduling algorithm for 5G Systems.

Design Requirements

Functional requirements are to ensure schedule and time allocation efficiency, ensure communication between base stations and the mobile core. The non-functional requirements are the research on 5G wireless Systems, srsRAN base code and the the documentation on srsRAN code.

We have an open-source software platform (srsRAN) and test are run on testbed.

Intended users: Undergrad Students, Graduate Students, and Professors

Intended uses: The goal was to obtain research regarding the UCS algorithm's relative function to base scheduler functions for other researchers to use in more commercial research, however, upon not being able to reach that goal we shifted to creating documentation for future senior design groups to help them avoid the massive time sink we had go through.

Technical Details

Our project was primarily software with some hardware support towards the end. For our software resources we were using srsRAN's code base downloaded from github in the language C++. For hardware we were given 2 SDRs and 1 monitor by our faculty advisor in order to try to test our algorithm in a physical environment.

Testing

```

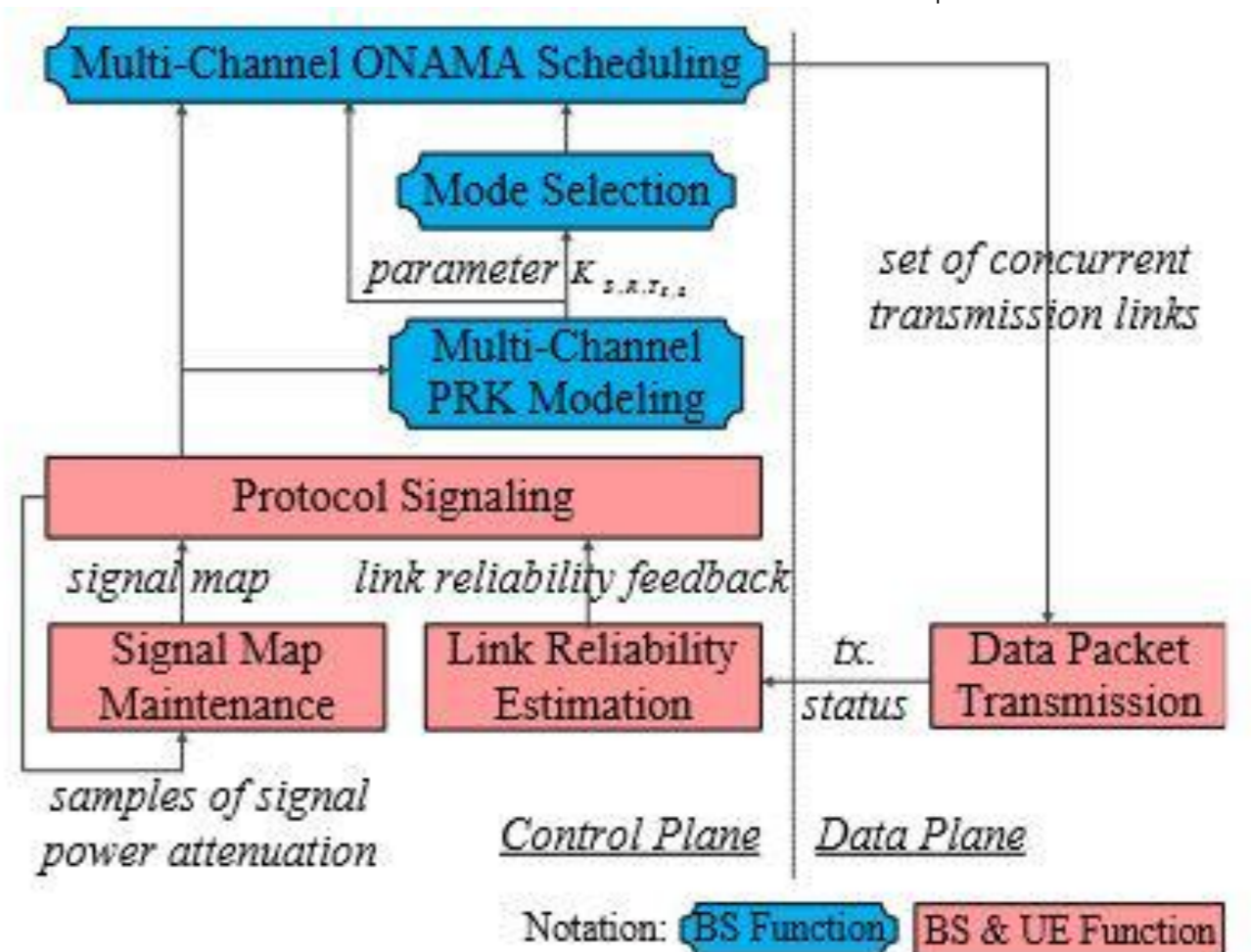
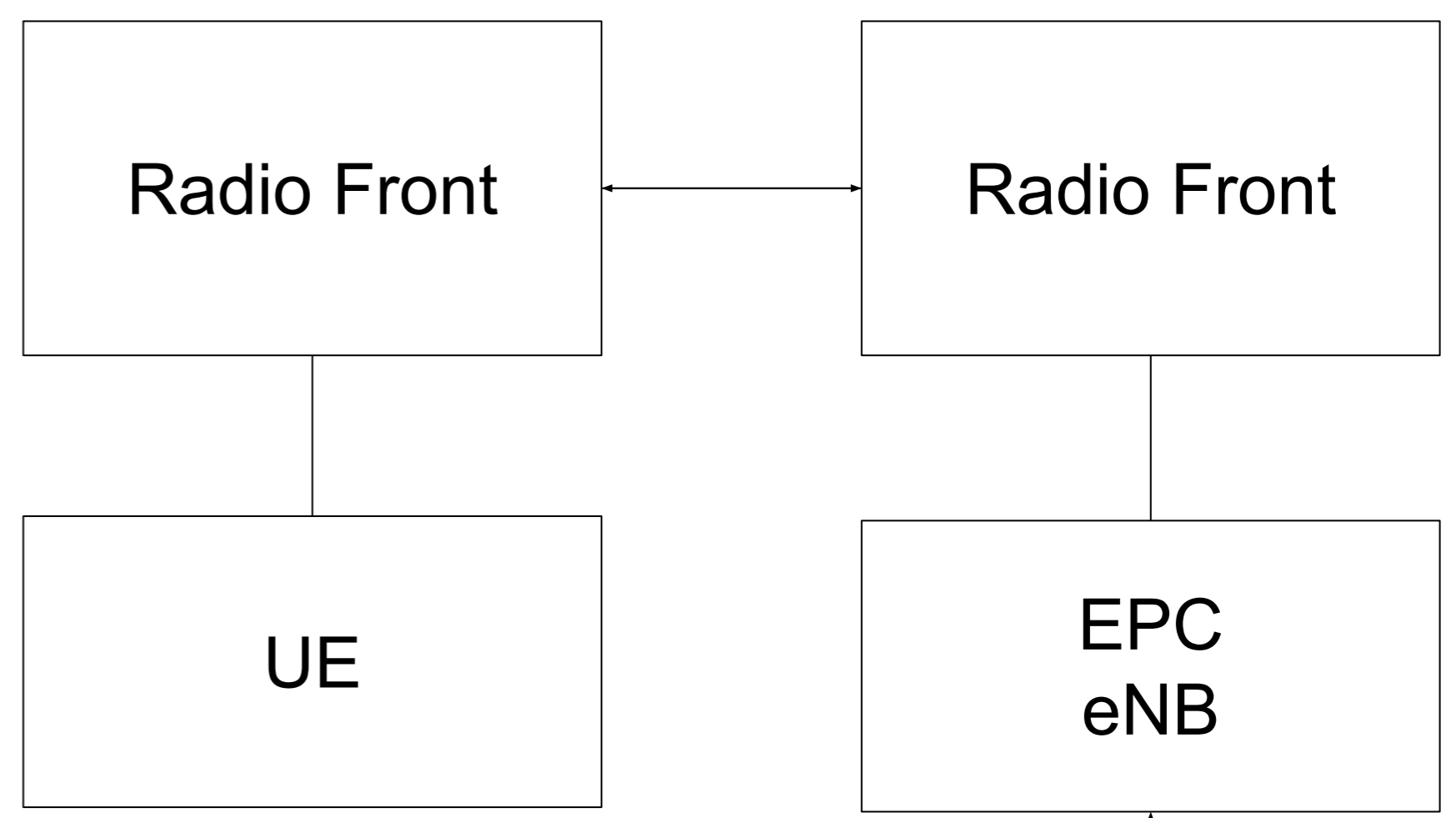
root@kali:~/srsRAN# ./iperf -s -l 1000
server listening on UDP port 5688
receiving 1470 byte datagrams
UD buffer size: 65536 bytes (default)

[ 0] local 172.16.0.1 port 5688 connected with 172.16.0.3 port 5688
10) Interval: Transfer: Bandwidth: Jitter: Loss/Focal Datagram
11) 0.0-0.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.302 ms 10720/10720 (98%)
12) 1.0-1.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.378 ms 8726/89160 (98%)
13) 2.0-2.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.375 ms 8726/89160 (98%)
14) 3.0-3.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.361 ms 8729/89163 (98%)
15) 4.0-4.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.407 ms 8729/89163 (98%)
16) 5.0-5.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.440 ms 8743/89255 (98%)
17) 6.0-6.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.326 ms 8729/89163 (98%)
18) 7.0-7.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.375 ms 8733/89167 (98%)
19) 8.0-8.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.433 ms 8729/89163 (98%)
20) 9.0-9.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.427 ms 8729/89163 (98%)
21) 10.0-10.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.365 ms 8730/89254 (98%)
22) 11.0-11.9 sec: 2.03 MBytes 21.7 Mbits/sec 0.392 ms 8732/89164 (98%)
23) 12.0-12.9 sec: 2.03 MBytes 22.0 Mbits/sec 0.401 ms 8730/89162 (98%)
24) 13.0-13.9 sec: 2.03 MBytes 20.9 Mbits/sec 0.499 ms 8074/88551 (98%)
25) 14.0-14.9 sec: 2.03 MBytes 19.7 Mbits/sec 0.555 ms 8840/89158 (98%)
26) 15.0-15.9 sec: 2.21 MBytes 18.5 Mbits/sec 1.842 ms 8420/85782 (98%)
27) 16.0-16.9 sec: 2.32 MBytes 19.7 Mbits/sec 0.598 ms 9360/93519 (98%)
28) 17.0-17.9 sec: 2.50 MBytes 21.0 Mbits/sec 0.259 ms 8783/89448 (98%)
29) 18.0-18.9 sec: 1.98 MBytes 18.0 Mbits/sec 1.297 ms 7627/76242 (98%)
30) 19.0-19.9 sec: 2.00 MBytes 17.5 Mbits/sec 0.084 ms 9427/93874 (98%)
31) 20.0-20.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.277 ms 8443/85309 (98%)
32) 21.0-21.9 sec: 0.88 MBytes 5.92 Mbits/sec 3.271 ms 4823/48765 (99%)
33) 22.0-22.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
34) 23.0-23.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
35) 24.0-24.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
36) 25.0-25.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
37) 26.0-26.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
38) 27.0-27.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
39) 28.0-28.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
40) 29.0-29.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
41) 30.0-30.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
42) 31.0-31.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
43) 32.0-32.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
44) 33.0-33.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
45) 34.0-34.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
46) 35.0-35.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
47) 36.0-36.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
48) 37.0-37.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
49) 38.0-38.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
50) 39.0-39.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
51) 40.0-40.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
52) 41.0-41.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
53) 42.0-42.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)
54) 43.0-43.9 sec: 1.54 MBytes 16.0 Mbits/sec 0.558 ms 8493/86006 (98%)
55) 44.0-44.9 sec: 1.78 MBytes 14.3 Mbits/sec 0.675 ms 9383/92248 (99%)

```

For the testing we needed to test for packet loss and bandwidth of both the default srsRAN with RR and PF scheduling as well as the newly implemented scheduling algorithm. To do this we installed srsRAN onto two compute nodes with two software defined radios, we then had to determine the transport layer protocol utilized by srsRAN and run an iperf command which is depicted above.

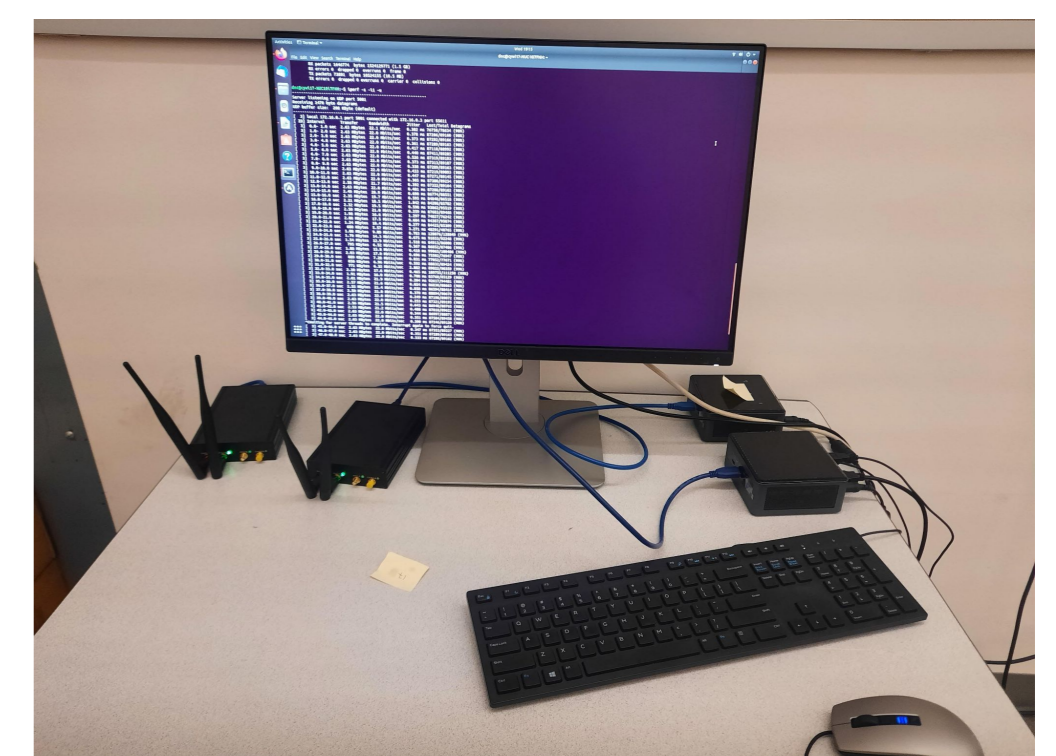
Design Approach



This diagram shows the architecture of the Unified Cellular Scheduling framework based on the status of uplink transmission as well as downlink and D2D link transmissions, the Bses and UEs estimate the communication reliability respectively

Resources

- 2 Radio fronts
- 2 PCs
- srsRAN code base



Standards

- IEEE and SESC software development standard
- Transparent on using open-source resources